

Fault tolerance analysis and measurement of a spatial power amplifier

S. Ortiz, M. Ozkar, A. Yakovlev, M. Steer and A. Mortazawi. "Fault tolerance analysis and measurement of a spatial power amplifier." 2001 MTT-S International Microwave Symposium Digest 01.3 (2001 Vol. III [MWSYM]): 1827-1830 vol.3.

The performance degradation due to device failures in a spatial power combining system is presented in this paper, including analysis and measurement. For this study, a 5/spl times/5 amplifier array, employing 17 dBm (P1 dB) MMIC amplifiers, has been designed and tested. Individual unit cells have been turned off in order to represent a system with device failures. Results for both simulated and measured array performance versus device failures are given.

 [Return to main document.](#)